

Quantum Computing: An Introduction for the perplexed

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IBM Distinguished Engineer

Master Inventor, Visiting Professor, FBCS

IBM Quantum Ambassador leader for UK





Our mission

Bring useful quantum
computing to the world

Make the world
quantum safe



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Our modern digital
world depends on
public key
cryptography

————— This is now a problem

The problem with quantum computers

Shor's algorithm (1994) shows a quantum computer can factor large numbers—at least 2048 bits long (that's 617 decimal digits).

On a classical computer, this would take millions of years.

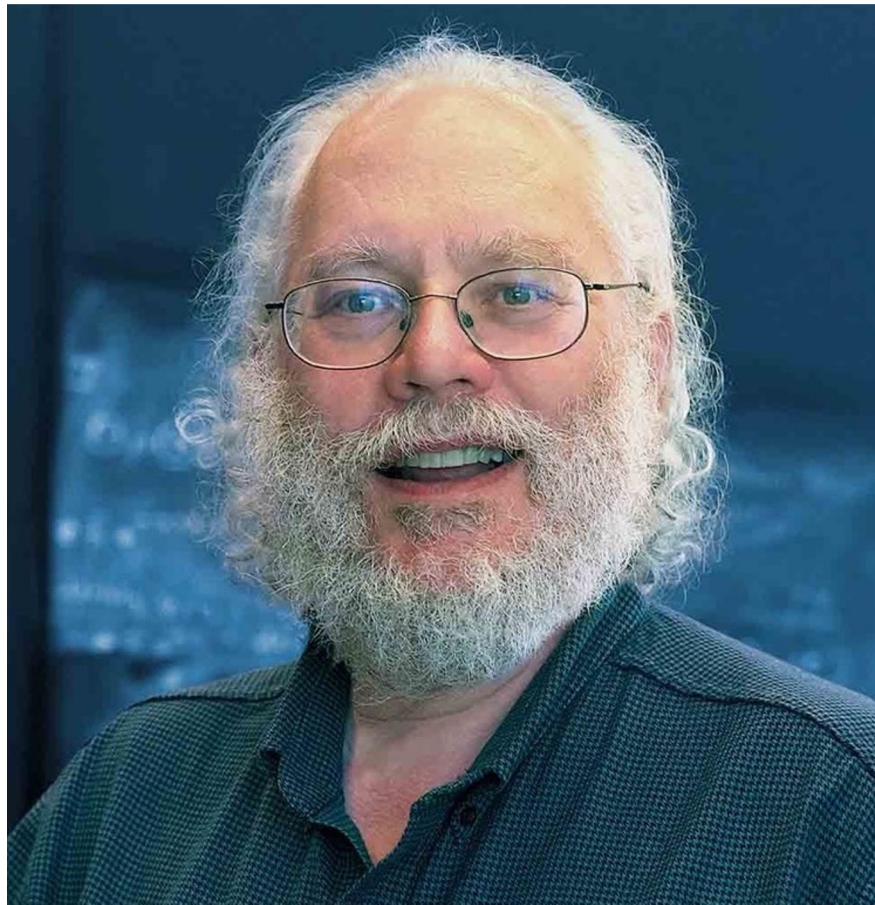
On a *cryptographically relevant quantum computer*, this would take a few hours.

Shor's algorithm breaks RSA and other public-key cryptosystems, where security is based on the difficulty of mathematical **trapdoor** functions (factoring large numbers or taking discrete logarithms).¹

Grover's algorithm (1996) theoretically affects AES, but probably won't in our lifetimes.²

1. "[Polynomial-Time Algorithms for Prime Factorization and Discrete Logarithms on a Quantum Computer](#)," 25 January 1996.

2. "[A fast quantum mechanical algorithm for database search](#)" 29 May 1996



Peter Shor of Bell Labs and MIT delivered the 2017 Viterbi Lecture, "Capacities for Quantum Communication Channels" Photo: Ming Hsieh, Department of Electrical and Computer Engineering at the University of Southern California.

Today's classical security protocols will be obsolete tomorrow

Prime factors

$$= p \times q$$

2048-bit composite integer

```
25195908475657893494027183240048398571429282126204032
02777713783604366202070759555626401852588078440691829
06412495150821892985591491761845028084891200728449926
8739280728776735971418347270261896375014971824691165
07761337985909570009733045974880842840179742910064245
86918171951187461215151726546322822168699875491824224
33637259085141865462043576798423387184774447920739934
23658482382428119816381501067481045166037730605620161
96762561338441436038339044149526344321901146575444541
78424020924616515723350778707749817125772467962926386
35637328991215483143816789988504044536402352738195137
863656439212010397122822120720357
```

Expected computation time

The most powerful computer today:

Millions of years

Shor's quantum algorithm:


Hours

Public key encryption • Digital signatures • Key exchange algorithms

RSA • DSA • ECC • ECDSA • DH

Journey to Quantum Safe

U.S. National Institute of Standards and Technology announced the first quantum-safe cryptography protocol standards for cybersecurity (July 2022), three of which were created by IBM in collaboration with industry and academic partners.

Purpose	Algorithm
Public-key Encryption and Key establishment Algorithms	CRYSTALS-Kyber
Digital Signature Algorithms	CRYSTALS-DILITHIUM
DSA (alternate)	Falcon
DSA (alternate)	SPHINCS+
NIST Selected Algorithms, July 5 th 2022. NIST recommended two primary algorithms to be implemented for most use cases: CRYSTALS-KYBER (key-establishment) and CRYSTALS-Dilithium (digital signatures).	



US Government

establishes timeline for transition to CNSA 2.0-compliant algorithms

z16

First Quantum-safe platform

GSMA Telco Consortium

Support industry transition to quantum-safe cryptography

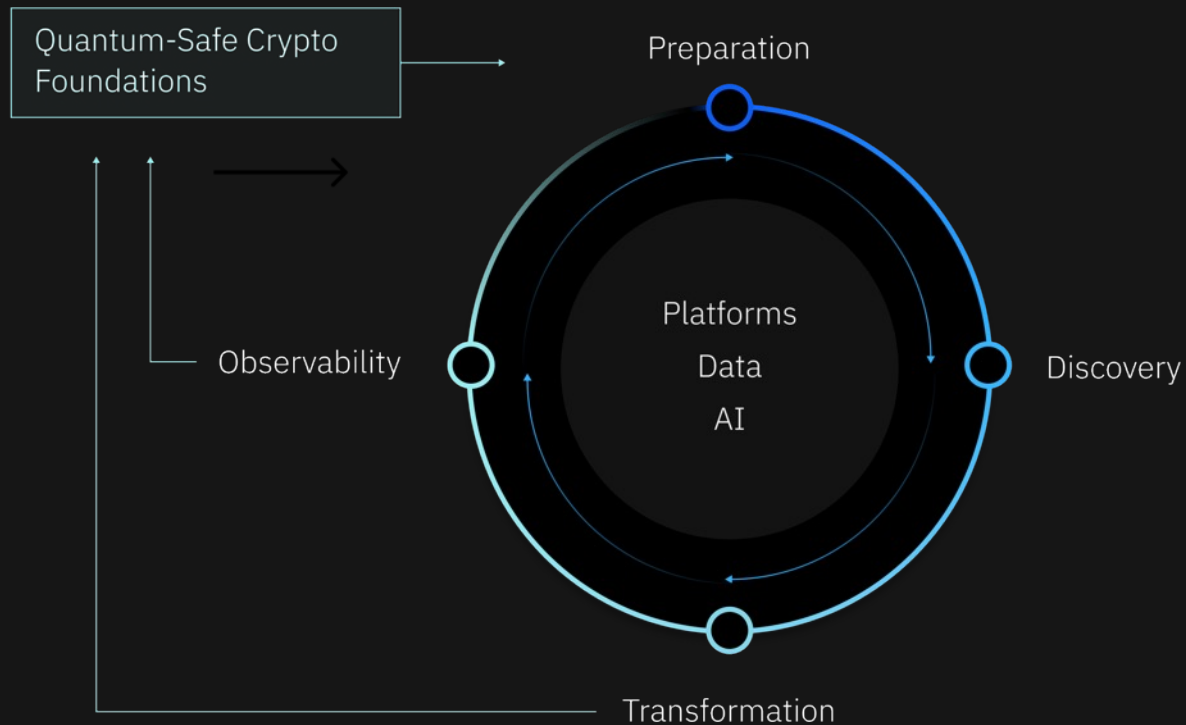
IBM Quantum Safe

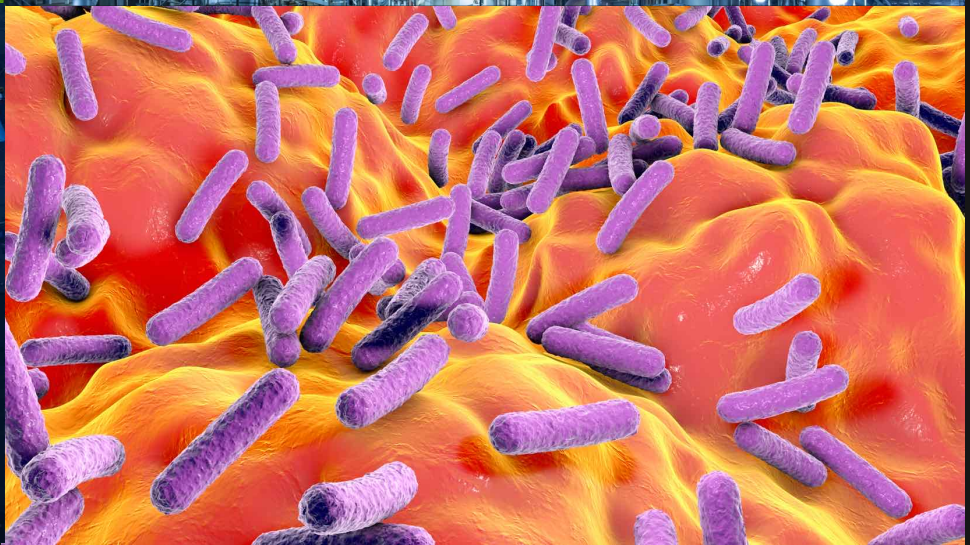
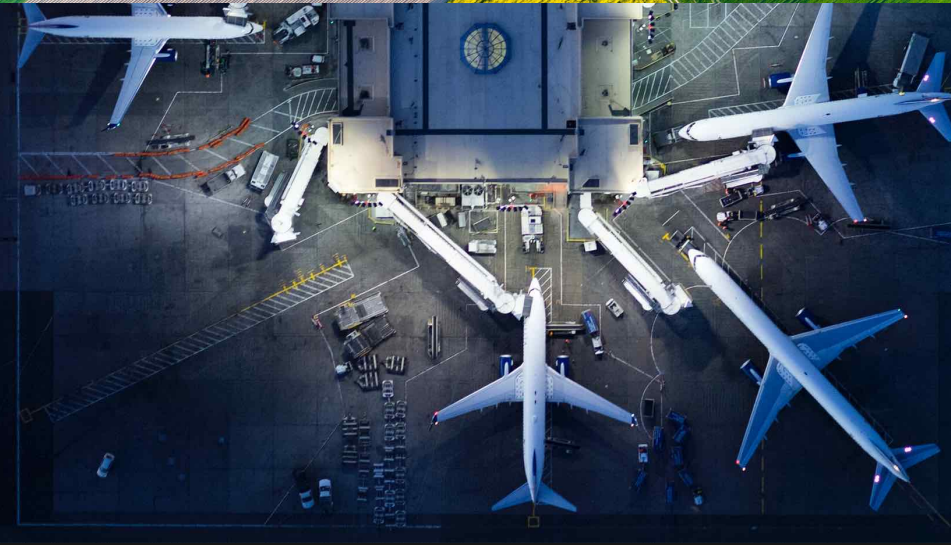
Support client transition to quantum-safe cryptography



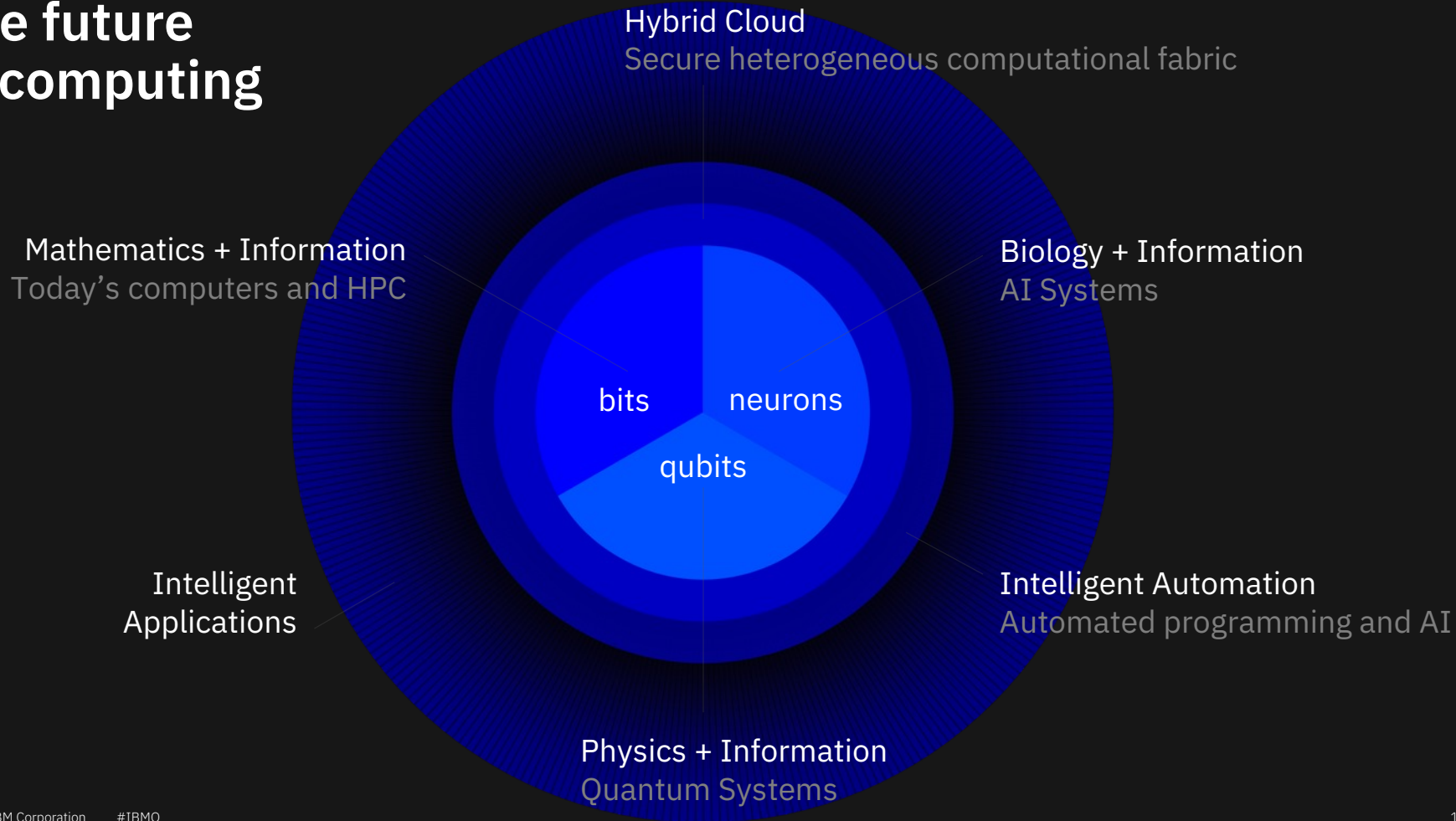
IBM Quantum Safe

- Understand, Prepare & Plan
- Discover & Classify Data
- Create a Crypto Inventory
- Implement with Crypto Agility





The future of computing



The limit of bits

For decades we've been simplifying nature into **1**s and **0**s because that was the only way we could manage to create a useful and scalable system of computation.

But the future isn't just **1**s and **0**s.

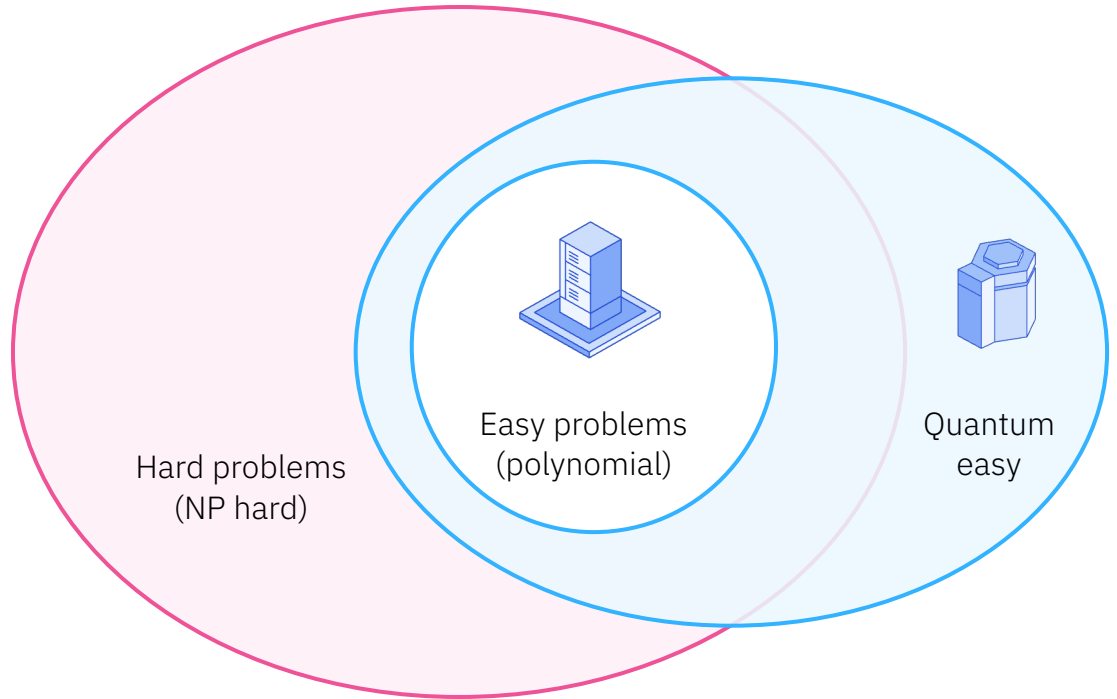
```
001001101110010010001001001001100100111
001011100111110010100100011100010001001
010001001001010101001010101110010011011
100100100010010010011001001110010111001
111100101001000111000100010010100010010
010101010010101011101110011100101011110
```

Quantum computers are the only novel hardware that changes the game

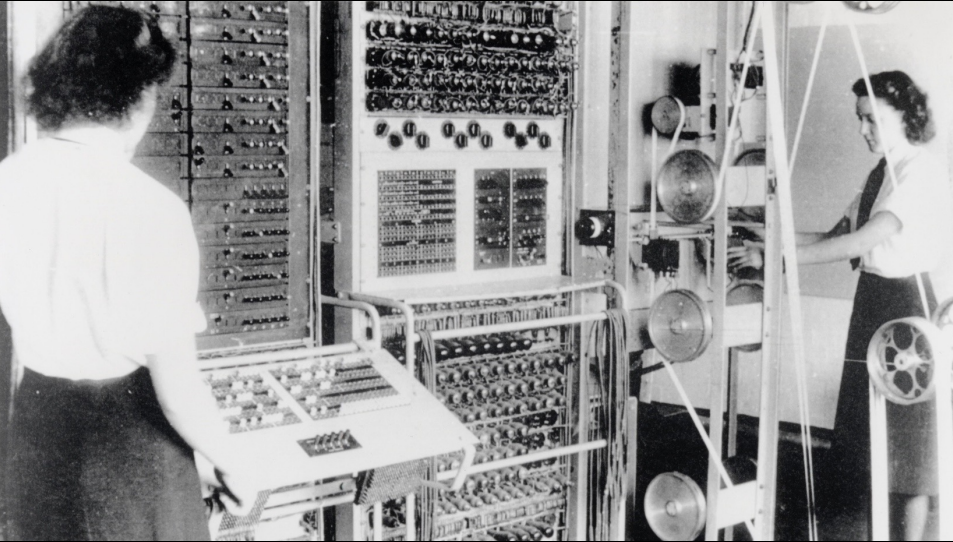
Quantum computing is not just a faster or better version of classical systems. **It is an entirely new branch of computing.**

Quantum computing follows the laws of nature to represent data in ways that mimic the randomness and unpredictability of the natural world.

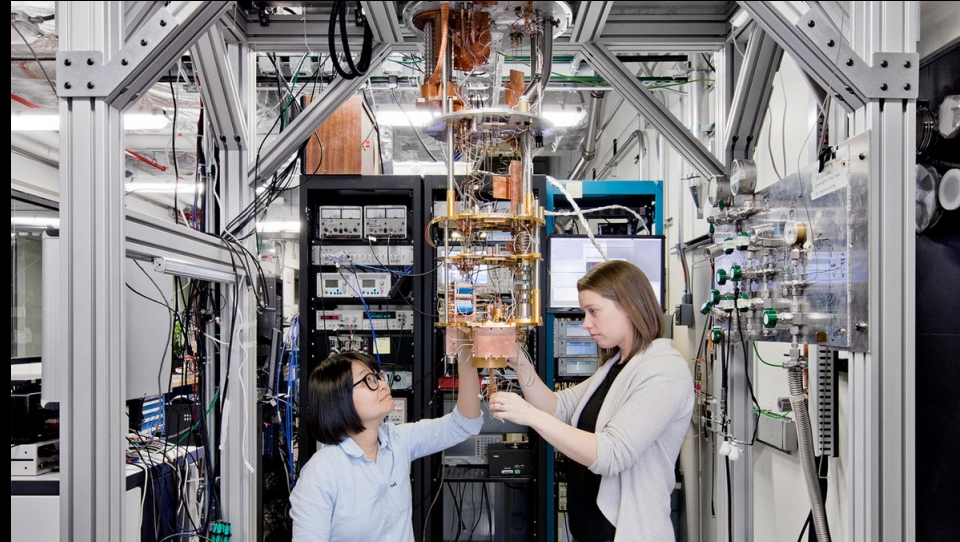
Ultimately, **GPUs and classical hardware are not built for this.**



We are in the early stages of a rapidly advancing new computing technology



Computer:
1944

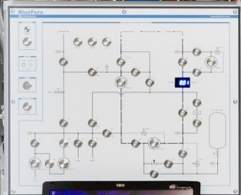


Quantum Computer:
2019



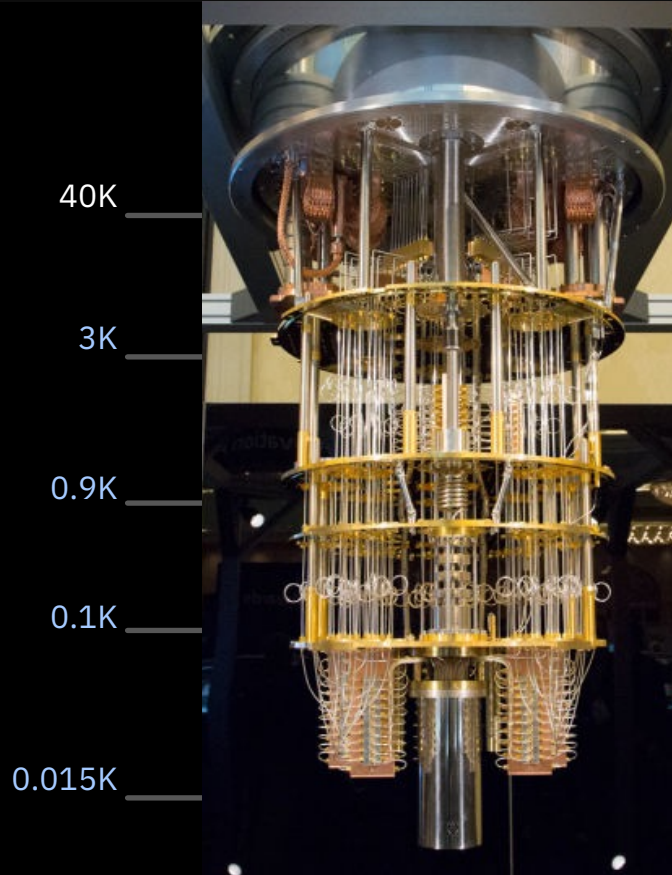
IBM Q

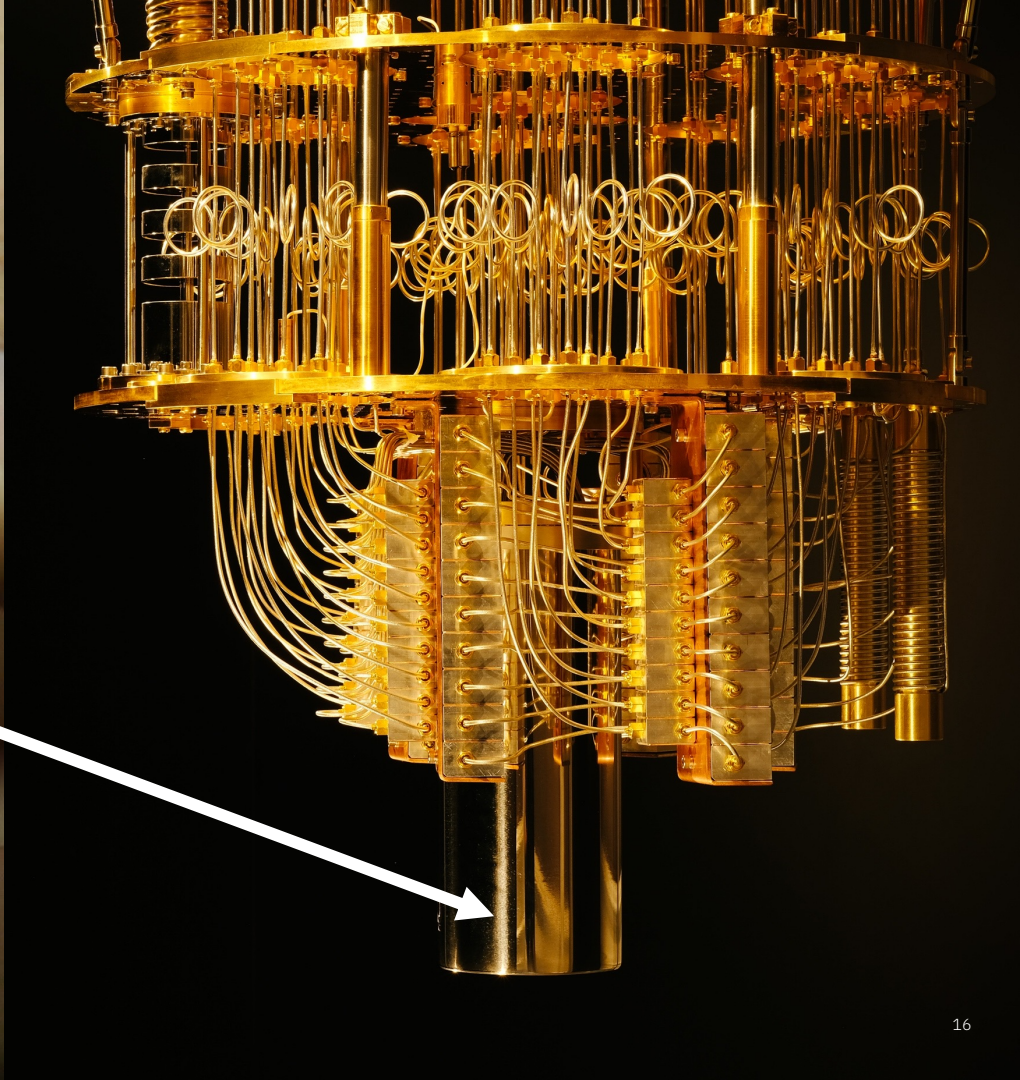
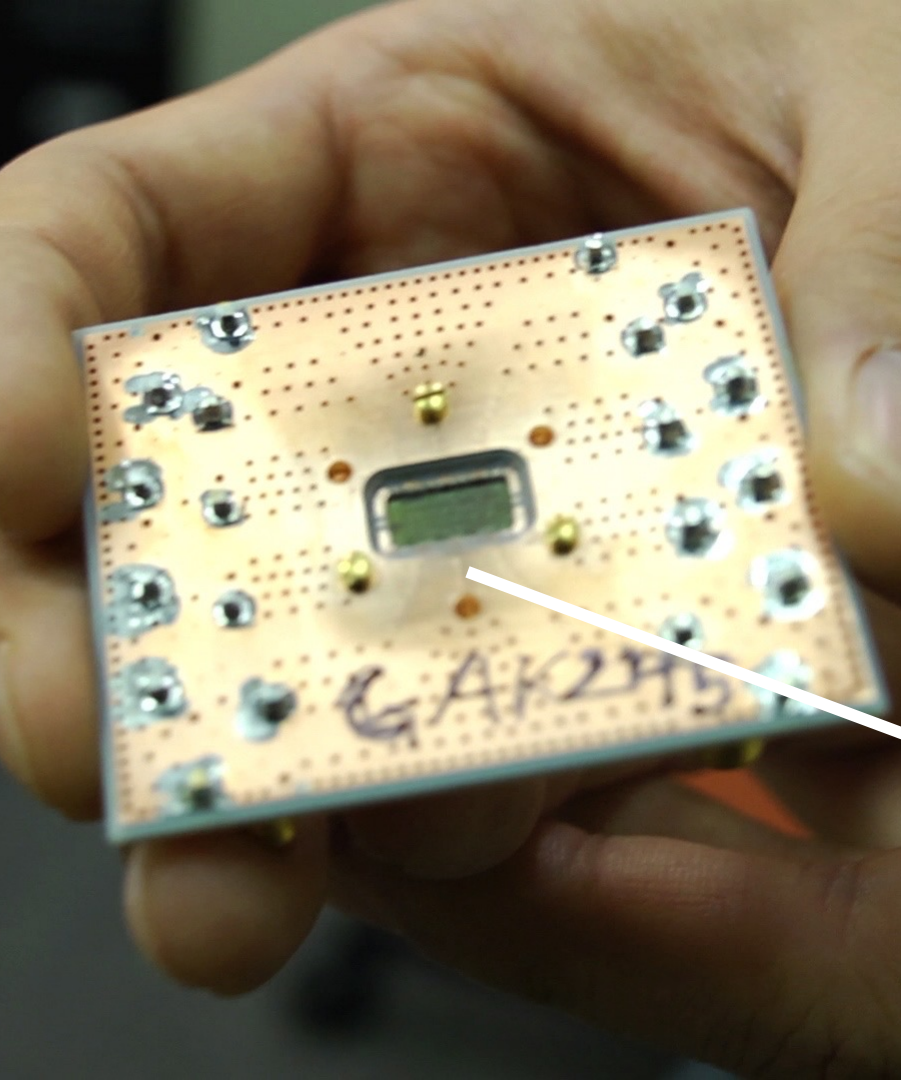
IBM Q



Superconducting qubits need to be really COLD

Refrigerator to cool qubits
to 10 - 15 mK with a
mixture of ^3He and ^4He
0.015 Kelvin – colder than
space





IBM

IBM Q
System One



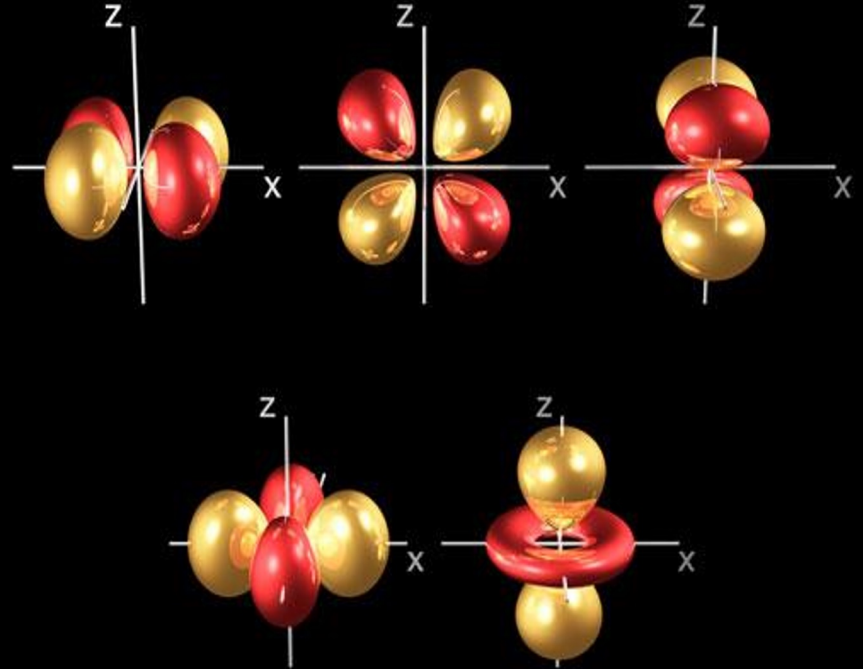
IBM

IBM Quantum
System One

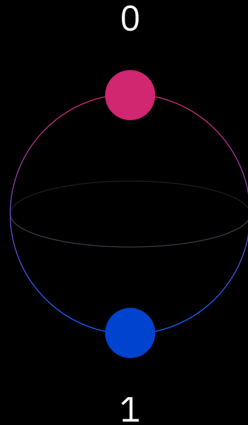


Quantum Particles

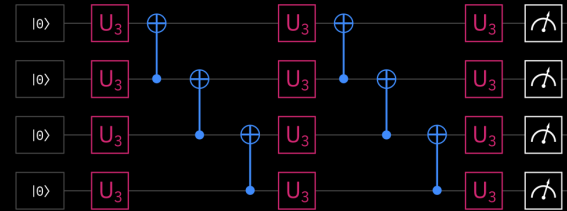
THERE ARE
10 KINDS
OF PEOPLE:
THOSE WHO
UNDERSTAND
BINARY AND
THOSE WHO **DON'T.**



Quantum bits and quantum circuits

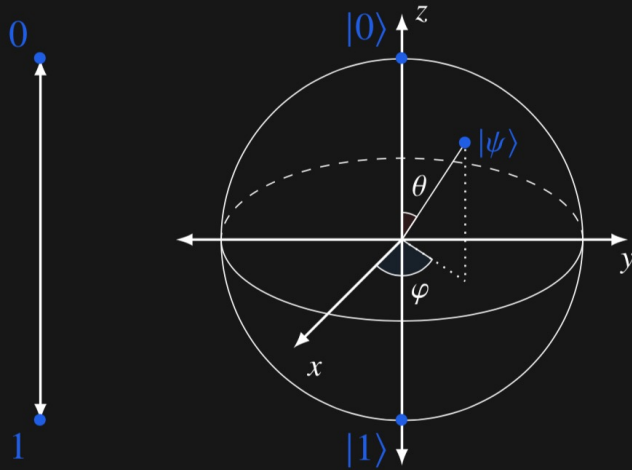


A quantum bit or **qubit** is a controllable quantum object that is the unit of information



A **quantum circuit** is a set of quantum gate operations on qubits and is the unit of computation

Quantum computing uses essential ideas from quantum mechanics



Superposition is creating a quantum state that is a combination of $|0\rangle$ and $|1\rangle$.

Measurement is an action that forces a qubit to either $|0\rangle$ or $|1\rangle$ based on probability.

Entanglement strongly connects two or more qubits so that their quantum states are no longer independent.

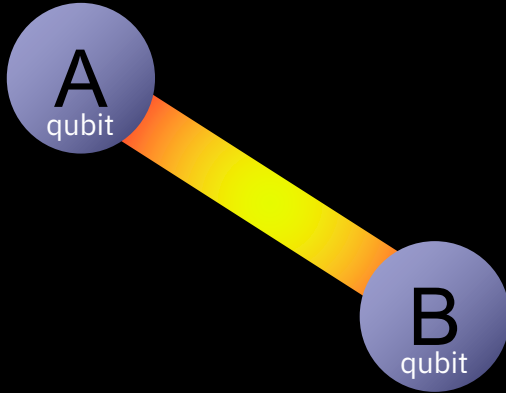
Quantum gates perform the basic computational operations on qubits (think of as counterparts to classical **and**, **+**, and so on).

Interference allows us to increase the probability of getting the right answer and decrease the chance of getting the wrong one.

Entanglement: “Spooky action at a distance”

We can also combine qubits to cause a correlation of results when observed.

Is the system observed as 1 or 0 ?



A: 50% of the time the answer is 1
50% of the time the answer is 0

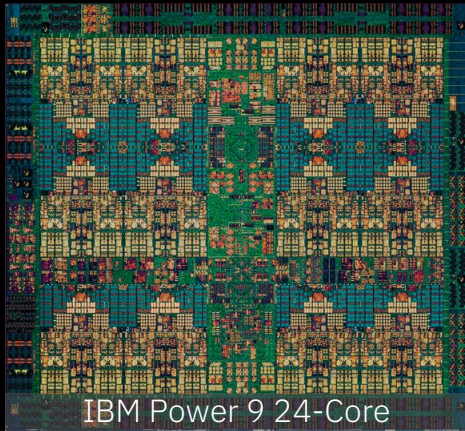
B: Gives the same random answers
as qubit A

Yet we can still perform deterministic operations on the two qubits.

The power of quantum computing

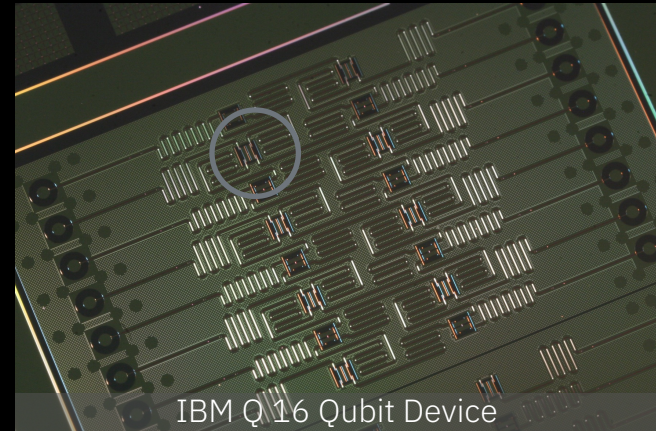
Classical Computers

The potential power of a classical computer doubles every time you double the number of transistors.



Quantum Computers

The potential power of a quantum computer doubles every time you add one additional qubit.



Exponential growth

$$2^n$$

n qubits – 2^n quantum state dimensions.

$$2^{10} = 1,024$$

$$2^{20} = 1,048,576$$

$$2^{50} = 1,125,899,906,842,624$$

$$2^{65} = 36,893,488,147,419,103,232$$

$$2^{127} = 170,141,183,460,469,231,731,687,303,715,884,105,728$$

Exponential growth

2^{275}

275 qubits – more quantum state dimensions than there are atoms in the observable universe.

60,708,402,882,054,033,466,233,184,58
8,234,965,832,575,213,720,379,360,039,
119,137,804,340,758,912,662,765,568

$\sim 6.1 \times 10^{82}$

The path to useful quantum computing

Run quantum circuits faster on quantum hardware

Chart a path to develop quantum technology (hardware + software) that runs noise-free estimators of quantum circuits faster than can be done using classical hardware alone.

Map interesting problems to quantum circuits

We need applications that can be solved only with quantum circuits that are known to be difficult to simulate. This must be done in partnership with our clients and users.

The three key metrics for measuring quantum computing performance



Scale

Measured by **number of qubits** which indicates the amount of information we can encode in the quantum system.

High coherence, high reliability, lower cost


2020	2021	2022
65 qubits	127 qubits	433 qubits



Quality

Measured by **Quantum Volume** which indicates quality of circuits and how faithfully circuits are implemented in hardware.

Need low operation errors, meaning large Quantum Volume

2020	Today	2022
32 QV	512 QV	1024 QV 



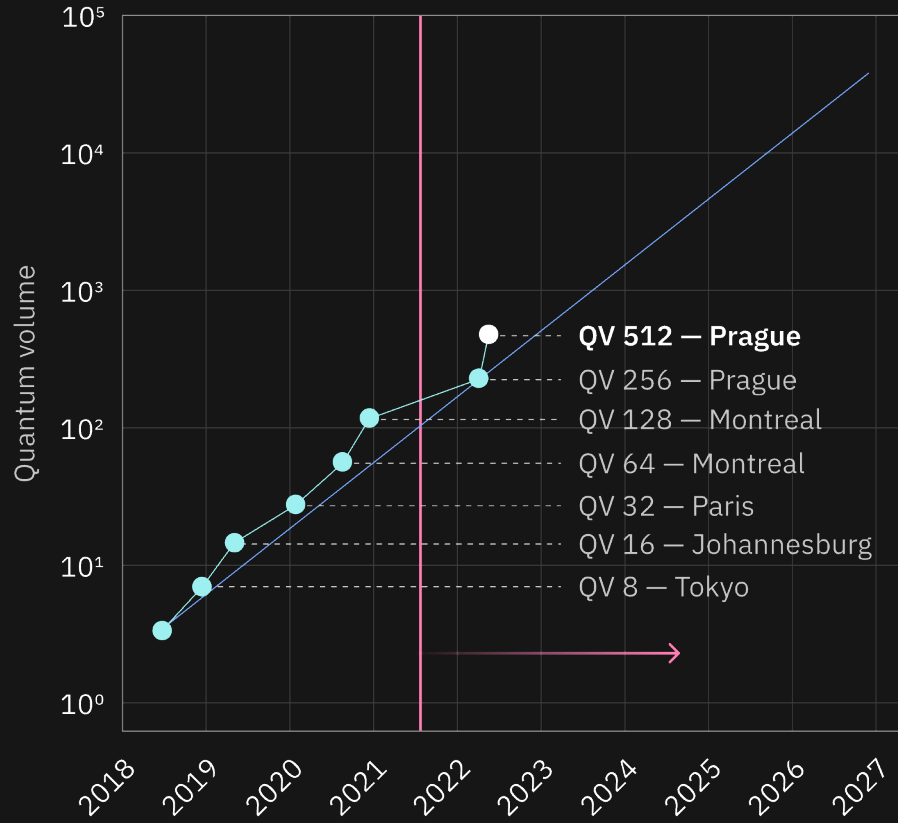
Speed

Measured by **CLOPS (Circuit Layer Operations Per Second)** which indicates how many circuits can run on hardware in a given time.

Seamless synchronization of quantum and classical circuits increases execution rate

2020	Today	2022
200 (Inferred)	1.4K CLOPS	10K CLOPS

Quantum volume



Quality

Coherence

Improved control

New architectures

PHYSICAL REVIEW LETTERS 127, 080505 (2021)

Tunable Coupling Architecture for Fixed-Frequency Transmon Superconducting Qubits

J. Stehlik,^{1,*} D.M. Zajac,^{1,*} D.L. Underwood,¹ T. Phung,² J. Blair,¹ S. Carnevale,¹ D. Klaus,¹ G.A. Keefe,¹

PHYSICAL REVIEW APPLIED 6, 064007 (2016)

Universal Gate for Fixed-Frequency Qubits via a Tunable Bus

David C. McKay,^{1,2} Stefan Filipp,² Antonio Mezzacapo,¹ Easwar Magesan,¹ Jerry M. Chow,¹ and Jay M. Gambetta¹

¹IBM T.J. Watson Research Center, Yorktown Heights, New York 10598, USA

²IBM Research-Zurich, 8803 Rueschlikon, Switzerland

(Received 26 April 2016; revised manuscript received 18 August 2016; published 12 December 2016)

A challenge for constructing large circuits of superconducting qubits is to balance addressability, coherence, and coupling strength. High coherence can be attained by building circuits from fixed-frequency qubits; however, leading techniques cannot couple qubits that are far detuned. Here, we introduce a method based on a tunable bus which allows for the coupling of two fixed-frequency qubits even at large detunings. By parametrically oscillating the bus at the qubit-qubit detuning we enable a resonant exchange ($XX + YY$) interaction. We use this interaction to implement a 183-ns two-qubit $i\text{SWAP}$ gate between qubits separated in frequency by 854 MHz, with a measured average fidelity of 0.9823(4) from interleaved randomized benchmarking. This gate may be an enabling technology for surface-code circuits and for analog quantum simulation.

DOI: 10.1103/PhysRevApplied.6.064007

Development Roadmap

	2016–2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2033+
	Run quantum circuits on the IBM Quantum Platform	Release multi-dimensional roadmap publicly with initial aim focused on scaling	Enhancing quantum execution speed by 100x with Qiskit Runtime	Bring dynamic circuits to unlock more computations	Enhancing quantum execution speed by 5x with quantum serverless and Execution modes	Improving quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhancing quantum execution speed and parallelization with partitioning and quantum modularity	Improving quantum circuit quality to allow 7.5K gates	Improving quantum circuit quality to allow 10K gates	Improving quantum circuit quality to allow 15K gates	Improving quantum circuit quality to allow 100M gates	Beyond 2033, quantum-centric supercomputers will include 1000's of logical qubits unlocking the full power of quantum computing
Data Scientist						Platform						
						Code assistant	Functions	Mapping Collection	Specific Libraries			General purpose QC libraries
Researchers						Middleware						
						Quantum Serverless	Transpiler Service	Resource Management	Circuit Knitting x P	Intelligent Orchestration		Circuit libraries
Quantum Physicist			Qiskit Runtime									
	IBM Quantum Experience		QASM3	Dynamic circuits	Execution Modes	Heron (5K)	Flamingo (5K)	Flamingo (7.5K)	Flamingo (10K)	Flamingo (15K)	Starling (100M)	Blue Jay (1B)
	Early	Falcon		Eagle		Error Mitigation	Error Mitigation	Error Mitigation	Error Mitigation	Error Mitigation	Error correction	Error correction
	Canary 5 qubits, Albatross 16 qubits, Penguin 20 qubits, Prototype 53 qubits	Benchmarking 27 qubits		Benchmarking 127 qubits		5k gates, 133 qubits, Classical modular, 133x3 = 399 qubits	5k gates, 156 qubits, Quantum modular, 156x7 = 1092 qubits	7.5k gates, 156 qubits, Quantum modular, 156x7 = 1092 qubits	10k gates, 156 qubits, Quantum modular, 156x7 = 1092 qubits	15k gates, 156 qubits, Quantum modular, 156x7 = 1092 qubits	100M gates, 200 qubits, Error corrected modularity	1B gates, 2000 qubits, Error corrected modularity

Innovation Roadmap

Software Innovation	IBM Quantum Experience	Qiskit	Application modules	Qiskit Runtime	Serverless	AI enhanced quantum	Resource management	Scalable circuit knitting	Error correction decoder			
	Circuit and operator API with compilation to multiple targets	Modules for domain specific application and algorithm workflows	Performance and abstract through Primitives	Demonstrate concepts of quantum centric-supercomputing	Prototype demonstrations of AI enhanced circuit transpilation	System partitioning to enable parallel execution	Circuit partitioning with classical reconstruction at HPC scale	Demonstration of a quantum system with real-time error correction decoder				
Hardware Innovation	Early	Falcon	Hummingbird	Eagle	Osprey	Condor	Flamingo	Kookaburra	Cockatoo	Starling		
	Canary 5 qubits, Albatross 16 qubits, Penguin 20 qubits, Prototype 53 qubits	Demonstrate scaling with I/O routing with Bump bonds	Demonstrate scaling with multiplexing readout	Demonstrate scaling with MLW and TSV	Enabling scaling with high density signal delivery	Single system scaling and fridge capacity	Demonstrate scaling with modular connectors	Demonstrate scaling with nonlocal c-coupler	Demonstrate path to improved quality with logical memory	Demonstrate path to improved quality with logical communication	Demonstrate path to improved quality with logical gates	
						Heron Architecture based on tunable-couplers	Crossbill m-coupler					

Executed by IBM
On target

Condor

Pushing the limits of scale & yield

1,121

Superconducting qubits

50%

Increase in qubit density

1 mile +

Of flex cabling



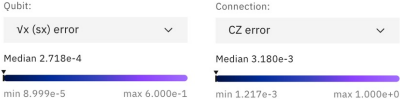
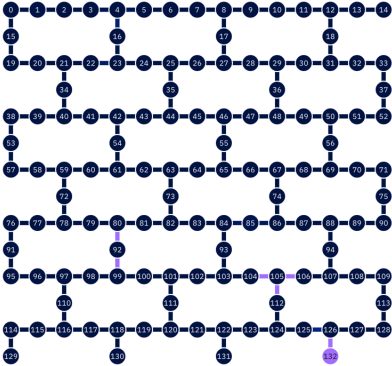
Introducing

Heron

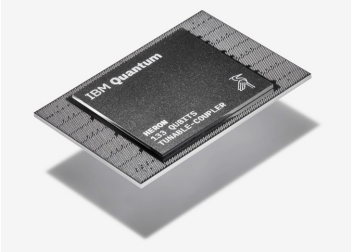
133 qubit systems

Tunable coupler architecture

ibm_montecarlo



	<i>ibm_sherbrooke Eagle</i>	<i>ibm_montecarlo (Heron)</i>
Gate Error (best system)	0.6-0.7%	0.3% - Best ~ 0.1%
Crosstalk	High (qubit-qubit collisions)	Almost zero!
Gate time	500-600ns	90-100ns

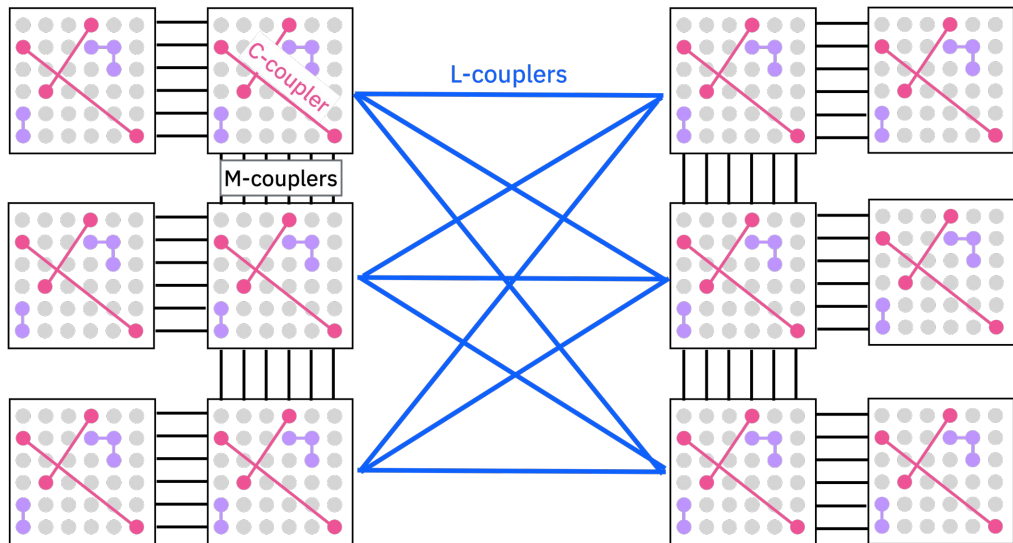


New error correction codes
and system modularity

C-coupler enables long-range
on-chip connections for high-rate
LDPC codes

L-coupler enables joining
multiple logical memories to
create large-scale systems

M-coupler enables joining multiple
short-range chip-to-chip connections



The path to useful quantum computing

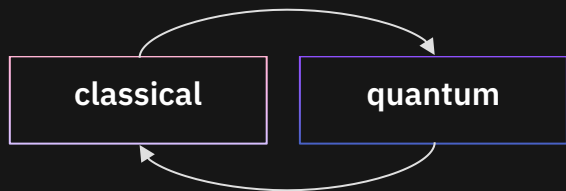
Run quantum circuits faster
on quantum hardware

Chart a path to develop quantum technology
(hardware + software) that runs noise-free
estimators of quantum circuits faster than can be
done using classical hardware alone.

**Map interesting problems to quantum
circuits**

We need applications that can be solved only with
quantum circuits that are known to be difficult to
simulate. This must be done in partnership with our
clients and users.

Structure of a typical workload



Real workloads are not purely quantum, but rather require **interaction** between quantum and classical compute resources.

Quantum applications span three general areas

Simulating Quantum Systems



Quantum chemistry
Material science
High energy physics

Artificial Intelligence



Better model training
Pattern recognition
Fraud detection

Optimization / Monte Carlo



Portfolio optimization
Risk analysis
Loans & credit scoring
Monte Carlo-like applications

Quantum applications span three general areas

Simulating Quantum Systems

Improved battery materials
 Manufacturing defect identification
 Semiconductor materials
 Chemical property prediction
 Drug Discovery
 Protein Structure Predictions
 Disease Risk Predictions

Accelerated Diagnosis
 Genomic Analysis
 Chemical product design
 Catalyst discovery
 Chemical process optimization
 High energy physics classification
 Transaction classification
 Product recommendation

Artificial Intelligence

Fraud detection
 Risk analysis
 Options pricing
 Derivatives Pricing
 Investment Risk Analysis
 Portfolio Management
 Transaction Settlement
 Finance Offer Recommender
 Credit/Asset Scoring
 Airline Scheduling

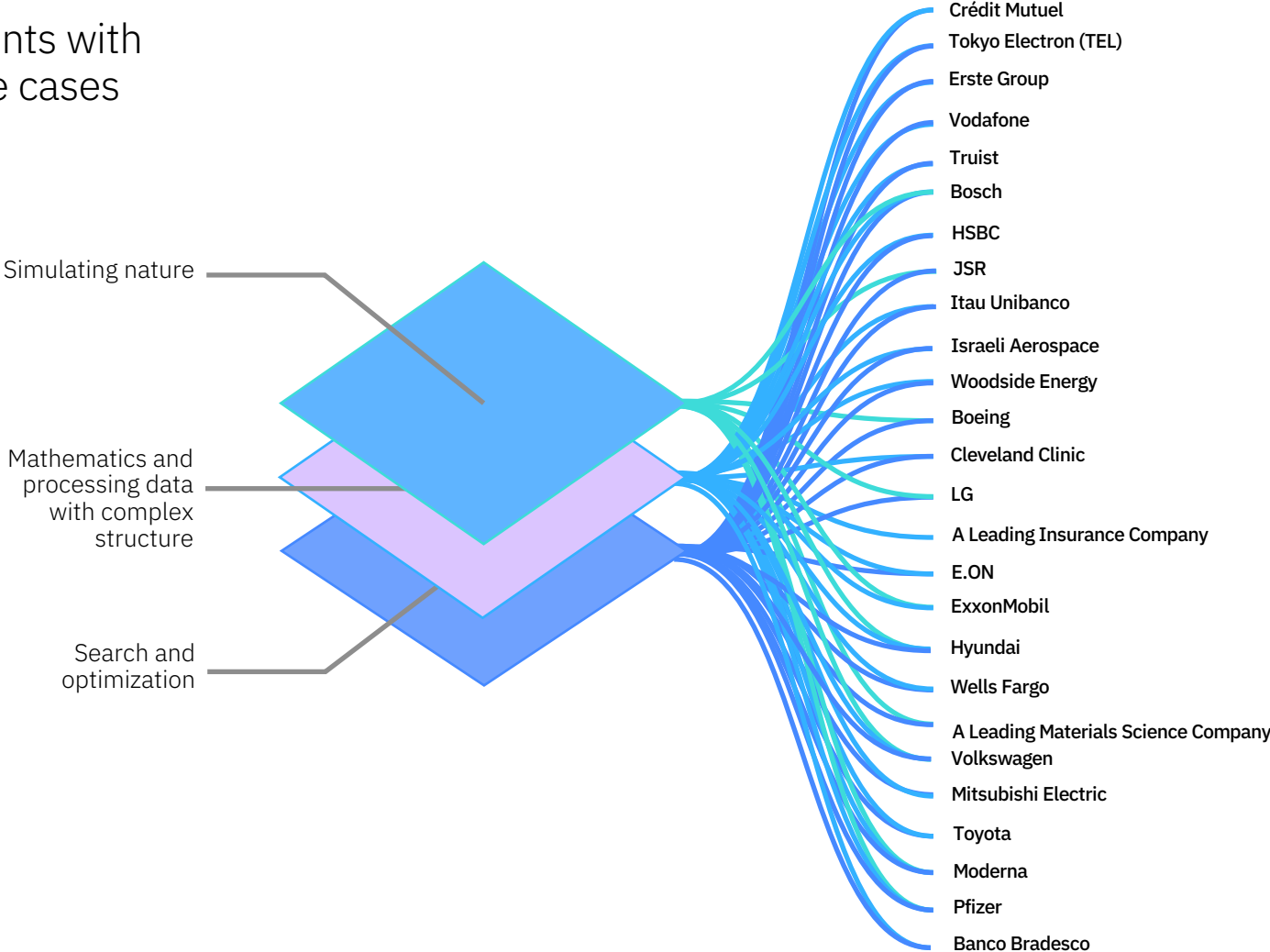
Optimization / Monte Carlo

Irregular Operations
 Network Optimization
 Product Portfolio Optimization
 Process Planning
 Quality Control
 Vehicle Routing
 Raw materials shipping
 Refining Processes
 Seismic imaging
 Disruption Management

Freight Forecasting
 Irregular Operations
 Fabrication Optimization
 Manufacturing Supply Chain
 Fluid Dynamics

and many more ...

Connecting industry clients with quantum computing use cases

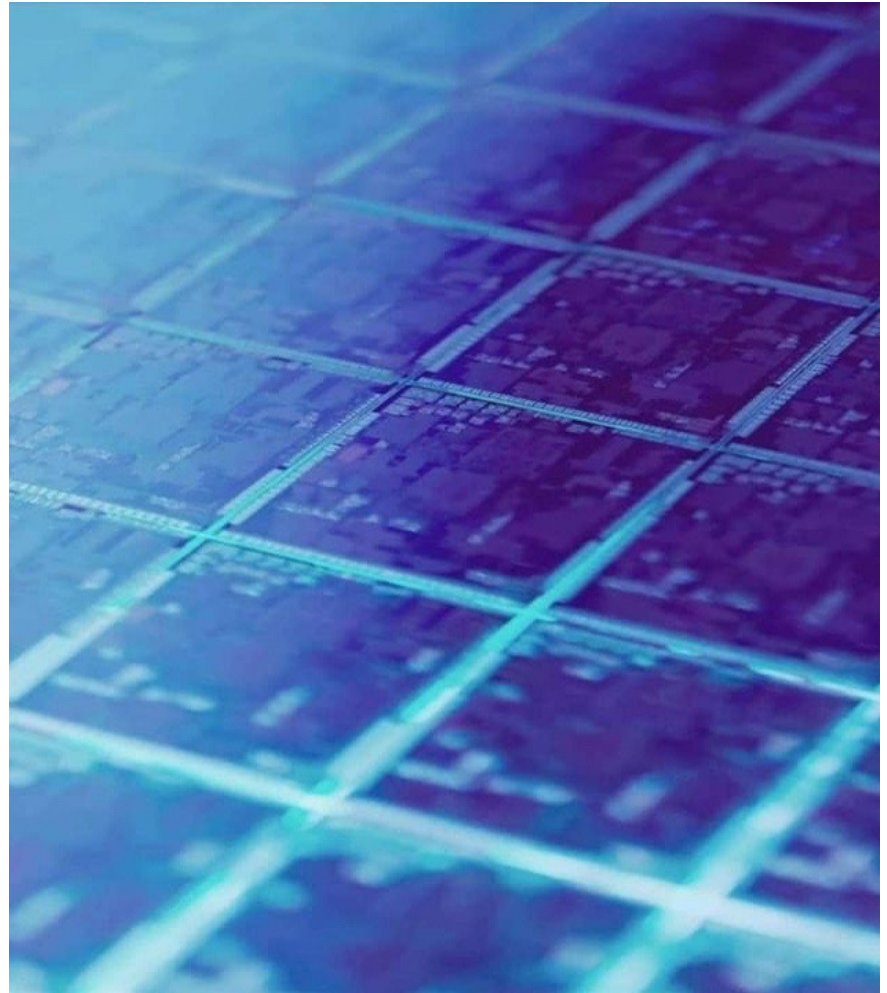


Wells Fargo

- Wells Fargo + IBM Quantum have published more than 10 quantum research papers in recent years.
- These papers explore new kinds of algorithms and new ways of thinking about problems in the financial services industry.
- Research focus on training quantum systems to mimic the probability distributions seen in the real world, then use that system for predictive modeling.

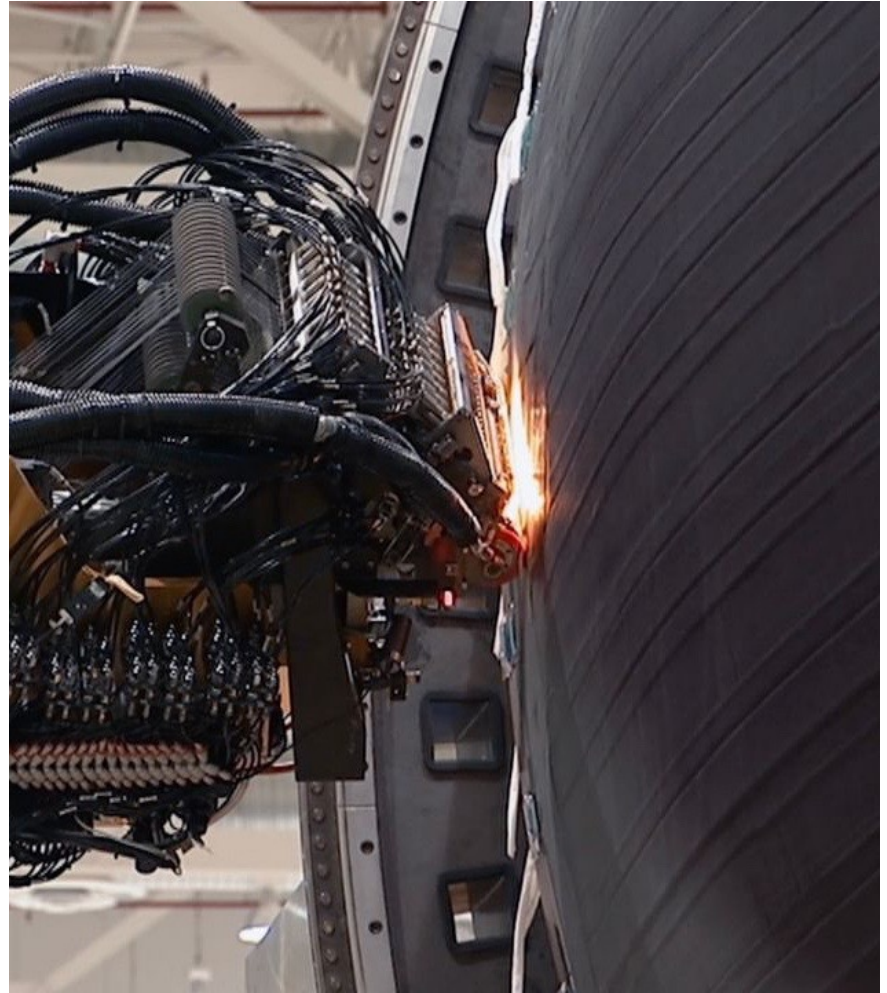


- JSR + IBM are experimenting with chemical simulations to help improve the development and manufacture of photoresists.
- We've already demonstrated that we can simulate small molecules that mimic parts of the photoresist.
- We hope that simulations like these will help us realize even faster chips.

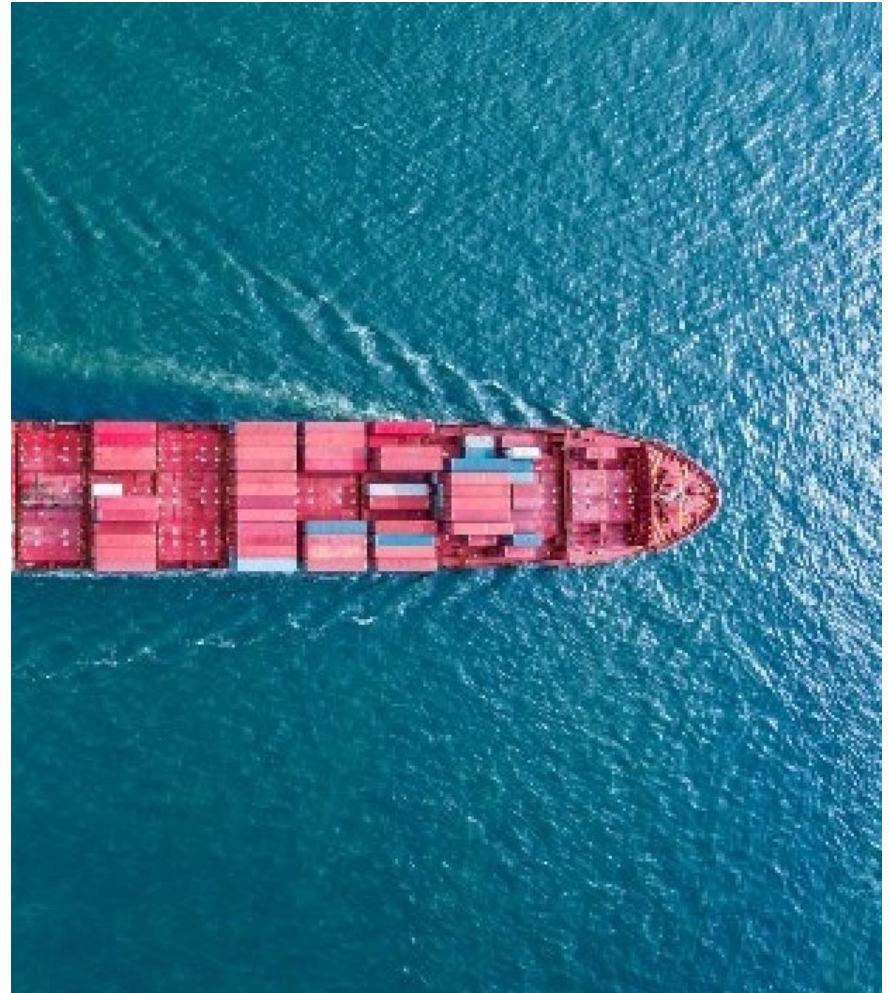


Boeing

- Boeing + IBM demonstrated a way to create larger computational spaces for simulating new materials.
- The simulations that the team ran are among the most complex quantum optimizations ever run.
- Boeing hopes these simulations will let them design lighter and stronger materials so planes can fly further with less fuel.



- ExxonMobil + IBM are modeling maritime inventory routing on quantum devices.
- By analyzing different strategies for vehicle and inventory routing they're laying the foundation for constructing practical solutions for their operations.
- More efficient shipping routes could help us transport fuel more efficiently around the globe.



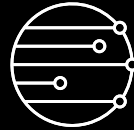
IBM Quantum Network

A collaborative community of
discovery

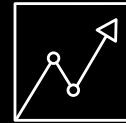
Educate and Train



Accelerate Research



Develop Applications



IBM Quantum Network members worldwide

IBM Quantum

1Qbit Systems	Consiglio Nazionale delle Ricerche - Istituto di calcolo e reti ad alte prestazioni	IIT	Netherlands Organization for Applied Scientific Research	Development Agency	Military University
AIQTECH Inc	Cornell University	Industrial Technology Research Institute	Netherlands eScience Center	Rahko	United States Naval Research Laboratory
Aalto University	Credit Mutuel	Infosys	NetraMark Corp	Saarland University	University of Amsterdam
Agnostiq Inc	DIC Corporation	Istituto Italiano di Tecnologia	NetraMark Corp	Samsung Advanced Institute of Technology	University of Basque Country
Aliro Quantum	Daimler	JP Morgan Chase	New Mexico State University	Sandia National Labs	University of Chicago
Amgen	Deloitte	JSR Corporation	New York University	Science and Technology Facilities Council Daresbury	University of Georgia
Anthem	Deutsches Elektronen Synchrotron	JoS Quantum	Nordic Quantum Computing Group	Seoul National University	University of Illinois at Urbana Champaign
Applied Quantum Computing	E.ON	Johns Hopkins University	North Carolina AT State University	SoftwareQ	University of Innsbruck
Apply Science	ETH Zurich	KEIO University	North Carolina State University	Solid State AI	University of Madrid
Argonne National Lab	Entangled Networks Ltd.	Keysight	Northeastern University	Sony	University of Melbourne
Arizona State University	Entropica Labs	Kipu Quantum	Northwestern University	Southern University and A&M College	University of Minho
Assured Information Security	Equal1	Korea Advanced Institute of Science and Technology	Oak Ridge National Lab	SpinUp AI	University of Montpellier
BP	Erste Group Bank AG	Korea Quantum Computing Corporation	Opacity	Stanford University	University of New Mexico
Beit	ExxonMobil	Korea University	Pacific Northwest National Lab	Stony Brook University	University of Oxford
Blueqat	Fermi National Accelerator Laboratory	LG Corporation	Phasecraft	Strangeworks	University of Sherbrooke
Boeing	Fidelity Investments	Lantik SA	Pohang University of Science and Technology	Sumitomo Mitsui Trust Bank Limited	University of South Carolina
Bosch	First Quantum	Lawrence Berkeley National Laboratory (Berkeley Lab)	Poznan Supercomputing and Networking Center	Sungkyunkwan University	University of Southern California
Boston University	Flightprofiler	Lockheed Martin	Prairie View AM University	Super Tech Labs	University of Tennessee
Bowie State University	Florida State University	Los Alamos National Laboratory	Princeton University	Surf	University of Tokyo
Boxcat Inc	Fraunhofer	Maastricht University	ProteinQure	Swiss Federal Institute of Technology Lausanne	University of Washington
Brookhaven National Lab	Fraunhofer members	Massachusetts Institute of Technology	Purdue University	System Vertrieb Alexander GmbH	University of Waterloo
Bundeswehr University Munich	GE Global Research	Max Kelsen	Q-Ctrl	TNO	University of Witwatersrand Johannesburg
CERN	General Atomics	Menten Al	QC Ware	Tech Mahindra Limited	University of the District of Columbia Community College
CMC Microsystems	Georgia Institute of Technology	Miraex	QEDMA Quantum Computing	The University of Texas at Austin	Virginia Tech
Cambridge Quantum Computing	Goldman Sachs	Mitsubishi Chemical Corporation	Qu & Co	Tokyo Electron Limited	Wells Fargo
Capgemini SE	HQS Quantum Simulations	Mitsubishi UFJ Financial Group	Quantfi	Toshiba	Woodside Energy Ltd
Carnegie Mellon Software Engineering Institute	HSBC	Mizuho Bank	Quantum MADS	Toyota	Xanadu
Centrum Wiskunde & Informatica	Hampton University	Molecular Forecaster Inc	Quantum Machines	Toyota Central RD Labs	Yokogawa Electric Corporation
Chalmers University of Technology	Hanyang University	Morehouse College	Quantum South	Turku University	Yonsei University
Classiq	Harvard University	Morgan State University	Quantum Technology Foundation of Thailand	Tuskegee University	Zapata Computing Inc
Clemson University	Hitachi Ltd	Multiverse Computing	QuantumNET	Ulsan National Institute of Science and Technology	Zurich Instruments
Cleveland Clinic Foundation	Horizon Quantum Computing	National Institute for Nuclear Physics	Quebec PINQ2	United States Air Force Research Lab	qBraid Co
Cognizant	Howard University	National Taiwan University	Qunasys	United States Naval Postgraduate	
ColdQuanta	III Taiwan	National University of Singapore	RIKEN National Research and		
ColibriTD	Indian Institute of Technology - Madras				

Dashboard

TOOLS

Results

Circuit Composer

Qiskit Notebooks

RESOURCES

Documentation & Support

back

New here? Get started with the IBM Q Experience!



Qiskit Notebooks

Create your first notebook and start using Qiskit

[Create a notebook →](#)

Circuit Composer

Explore the graphical interface for creating and testing circuits

[Create a circuit →](#)

Your backends

These are the quantum systems and simulators that you have access to.

[Got it!](#)

online

ibmq_16_melbourne (14 qubits)



Queue: 0 runs

ibm-q/open/main

online

ibmq_5_yorktown - ibmqx2 (5 qubits)



Queue: 1 runs

ibm-q/open/main

quantum-computing.ibm.com

A photograph of three scientists in a laboratory setting. They are focused on a large, complex, multi-tiered quantum computing device. The device is constructed from numerous vertical metal rods and horizontal gold-colored plates, with many small components and wires attached. The scientists are wearing blue gloves and are carefully examining and adjusting the device. The background is slightly blurred, showing other laboratory equipment.

What builds a quantum workforce?

OPEN ACCESS

IBM is the only company to offer our real quantum computers available for public and premium access via the cloud.

OPEN SOURCE

Written in Python and maintained on GitHub, Qiskit is designed to make quantum computing software tools and frameworks available to everyone.

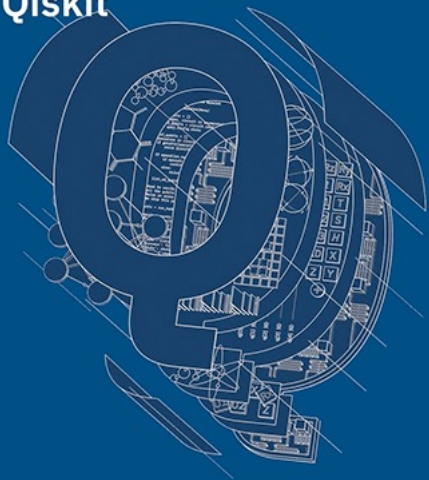
EDUCATION

Now is the opportunity for us all to give back and support building a diverse community of researchers, students, educators, and developers.

Open Source Textbook

community.qiskit.org/textbook

Learn Quantum
Computation using
Qiskit



Traditional Quantum Computation Course

Linear Algebra
Quantum Mechanics

Quantum Algorithms

Quantum Hardware

Learn Quantum Computation using Qiskit Textbook

Python
Qiskit

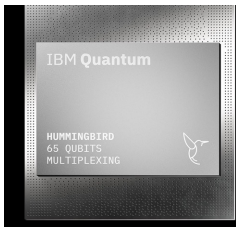
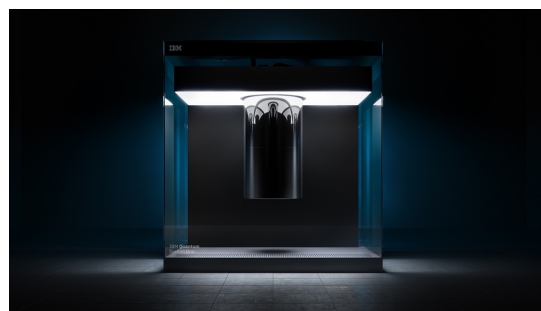
Quantum Programming

Quantum Algorithms on
Today's Hardware

Chapters:

0. Prerequisites
1. Quantum States and Qubits
2. Single Qubits and Multi-Qubit Gates
3. Quantum Algorithms
4. Quantum Algorithms for Applications
5. Investigating Quantum Hardware Using Qiskit
6. Implementations of Recent Quantum Algorithms

learning.quantum.ibm.com



2016 - 2023



2023 → Utility

We have entered
the era of
Quantum Utility.

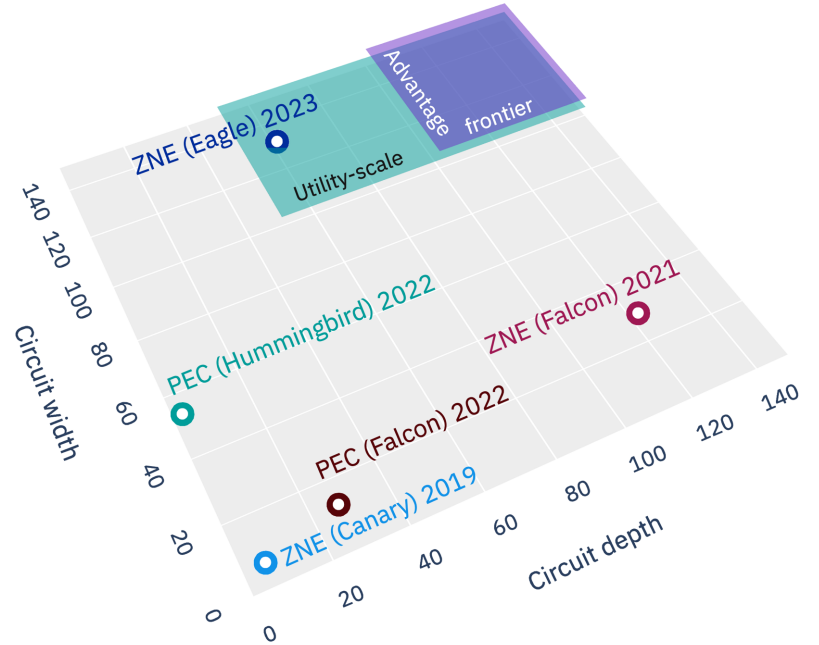
Definition of Quantum Utility

High-fidelity quantum computation outside the reach of exact classical simulation methods

It is the first major milestone on the path to Quantum Advantage.

Useful quantum computation requires utility-scale hardware and co-designed scalable software capabilities.

This means systems larger than 100 qubits, where simulations are not a viable alternative.



If you're not
using 100+ qubits,
you're not doing
quantum.

quantum-computing.ibm.com

IBM Quantum

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